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A diagram of a 4-bit shift register. It consists of four rectangular cells arranged horizontally. Above the first cell is an input 'A' with an arrow pointing into the cell. Above the second cell is an input 'B' with an arrow pointing into the cell. Above the third cell is an input 'C' with an arrow pointing into the cell. Above the fourth cell is an input 'D' with an arrow pointing into the cell. Below the first cell is an output 'A' with an arrow pointing out of the cell. Below the second cell is an output 'B' with an arrow pointing out of the cell. Below the third cell is an output 'C' with an arrow pointing out of the cell. Below the fourth cell is an output 'D' with an arrow pointing out of the cell. The entire register is enclosed in a larger rectangular frame.

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〒100-0001 東京都千代田区千代田 1-1-1  
TEL 03-5654-1280 FAX 03-5654-1380

〒100-0001 東京都千代田区千代田 1-1-1  
TEL 03-5650-1031 <http://www.takaratomy.co.jp>